

GD135**50V, DC – 3.2GHZ, 135W GAN HEMT****FEATURES**

- Operating Frequency Range: DC to 3.2GHz
- Operating Drain Voltage: +50V
- Maximum Output Power (P_{SAT}): 150W
- Maximum Drain Efficiency: 72%
- Efficiency-Tuned P3dB Gain: 20dB
- Bare die shipped in Gel-Pak containers



3.48 x 0.8 mm Die

DESCRIPTION

The GD135 is an 150W (P3dB) unmatched discrete GaN-on-SiC HEMT which operates from DC to 3.2GHz on a 50V supply rail. The wide bandwidth of the GD135 makes it suitable for a variety of applications including cellular infrastructure, radar, communications, and test instrumentation, and can support both CW and pulsed mode of operations.

Bare die are shipped in Gel-Pak containers for safe transport and storage.

TYPICAL PERFORMANCE: POWER TUNED at P3dB, $T_A = 25^\circ\text{C}$ ⁽¹⁾

Parameter	1.7 GHz	2.0 GHz	2.3 GHz	2.6 GHz	3.2GHz
Gain (dB)	19.1	18.0	16.3	15.4	TBD
Saturated Output Power (W)	163	158	164	159	TBD
Drain Efficiency (%)	63	64	63	63	TBD

⁽¹⁾ $V_D = 50\text{V}$, $I_{DQ} = 250\text{mA}$

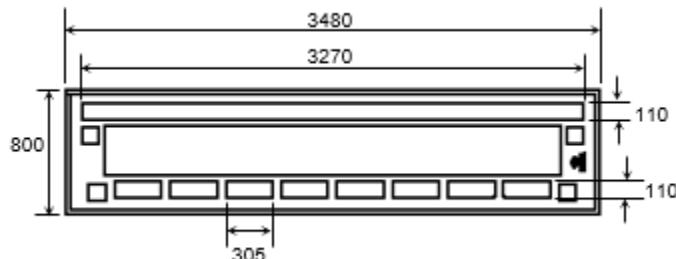
TYPICAL PERFORMANCE: EFFICIENCY TUNED at P3dB, $T_A = 25^\circ\text{C}$ ⁽²⁾

Parameter	1.7 GHz	2.0 GHz	2.3 GHz	2.6 GHz	3.2GHz
Gain (dB)	20.2	18.3	17.3	16.0	TBD
Saturated Output Power (W)	119	149	127	111	TBD
Drain Efficiency (%)	72	67	69	67	TBD

⁽²⁾ $V_D = 50\text{V}$, $I_{DQ} = 250\text{mA}$

GD135**50V, DC – 3.2GHZ, 135W GAN HEMT****ABSOLUTE MAXIMUM RATINGS**

Parameter	Rating	Units
Breakdown Voltage	>150	BV _{DG} (V)
Gate Source Voltage	-8 to +2	V _{GS} (V)
Operating Voltage	55	V (V)
Junction Temperature	+225	(°C)
Storage Temperature	-65 to +150	(°C)

BLOCK DIAGRAM (units in microns)**ELECTRICAL SPECIFICATIONS: T_A = 25°C**

Parameter	Min.	Typ.	Max.	Units	Notes
Frequency Range	DC		3200	MHz	
DC Characteristics					
Drain Source Breakdown Voltage	150			V _{DS} (V)	
Drain Source Leakage Current	1.50			I _{DS} (mA)	
Gate Threshold Voltage	-3.5 to -1.5			V _{GS} (V)	
Operating Conditions					
Gate Voltage	-2.5			V _G (V)	
Drain Voltage	50			V _D (V)	
Quiescent Drain Current	250			I _{DQ} (mA)	
Thermal Characteristics					
Thermal Resistance	TBD			(°C/W)	

GD135

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GaN HEMT BIASING SEQUENCE

To turn the transistor ON

1. Set V_{GS} to -5V
2. Turn on V_{DS} to normal operation voltage (50V)
3. Slowly increase V_{GS} to set I_{DS} current (250mA)
4. Apply RF power

To turn the transistor OFF

1. Turn the RF power off
2. Decrease V_{GS} to -5V
3. Turn off V_D . Wait a few seconds for drain capacitor to discharge
4. Turn off V_{GS}

CONTACT INFORMATION

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